

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. IMEC193.001AUS	APPLICATION NO. 09/760129
	APPLICANT Miranda, et al.	
	FILING DATE January 12, 2001	GROUP Unknown 2122

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)

FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	Gupta, et al., Analysis of High-Level Address Code Transformations for Programmable Processors, Department of Information and Computer Science, University of California at Irvine, IMEC Lab., Kapeldreef 75, 3001 Leuven, Belgium
ARG	Liem, et al., Address Calculation for Retargetable Compilation and Exploration of Instruction-Set Architecture, Proceedings 33rd Design Automation Conference, 1996. pp. 547-600
ARG	Miranda, et al., High-level Address Optimisation and Synthesis Techniques for Data-Transfer Intensive Applications, IEEE Transactions on VLSI Systems, Vol. 6, No. 4, pp. 677-686, December 1998.

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EXAMINER	Andrew R. Fowler	DATE CONSIDERED	12/24/03
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